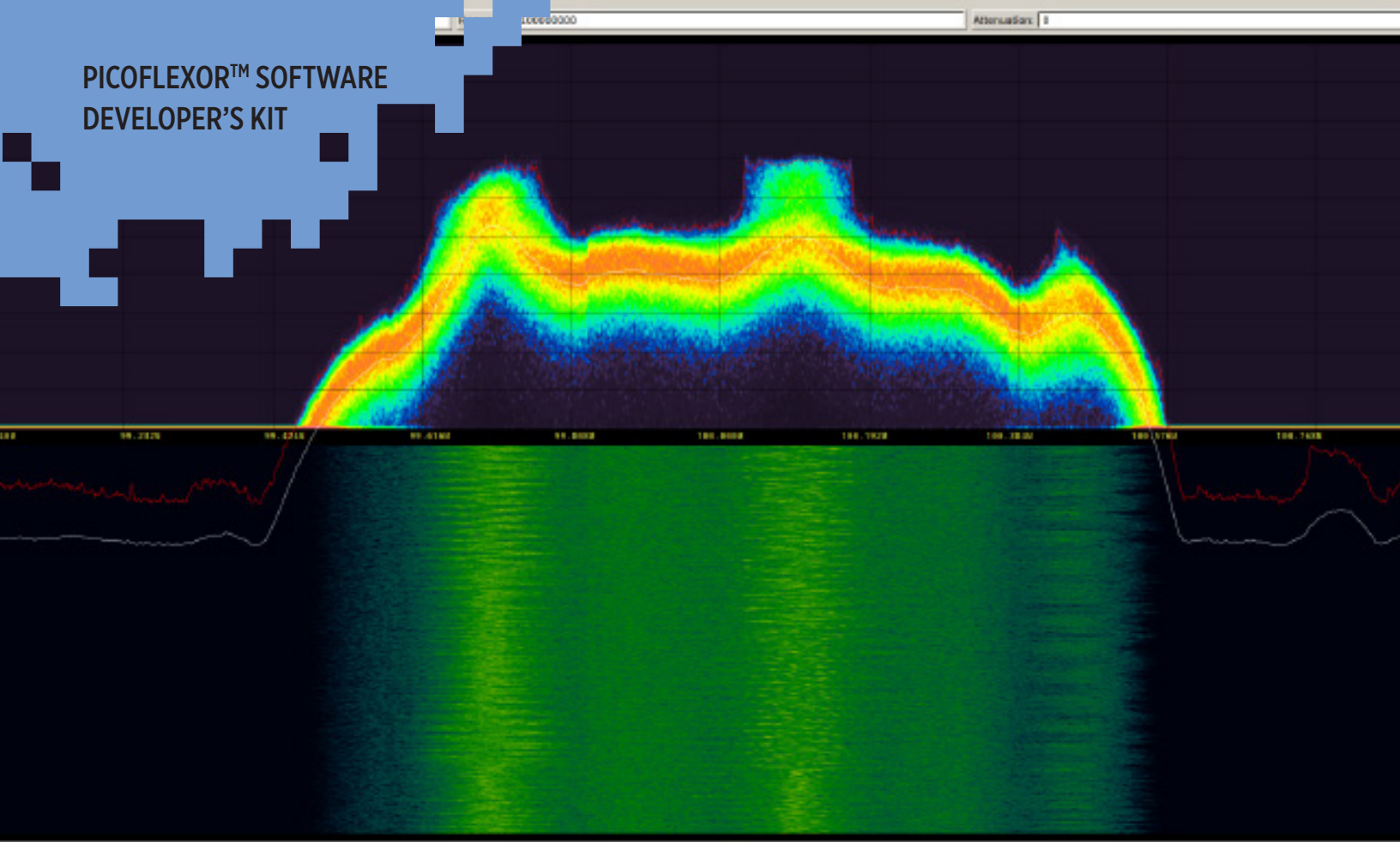


## PICOFLEXOR™ SOFTWARE DEVELOPER'S KIT



# FPGA APPLICATION DEVELOPMENT, DEBUGGING & DEPLOYMENT IN A SINGLE GUI

The 8649A/PF/SDK PicoFlexor™ Software Developer's Kit constitutes a Linux virtual machine (VM) complete with source code and tools required to perform field programmable gate array (FPGA) application development, software application development, debugging and deployment for the SI-8649A/PF PicoFlexor within a graphical development environment. The PicoFlexor is a miniature tactical software definable radio (SDR). It combines high RF performance, state of the art digital processing, and a simple development interface to create a best-in-class SIGINT SDR.

The Velocity integrated development environment (VIDE) is an Eclipse plug-in that provides a common application framework, which is technology and version-agnostic. As an Eclipse integrated development environment, it supports a number of programming languages and development paradigms. The VMware Build subsystem is supplied on a DVD set for installation on a host computer that is a compatible Linux or Windows machine.

The system builds the target System Software to include the Linux kernel, drivers and applications and it establishes the specification for the VM and the virtual Linux operating system. It can be edited to add serial ports or disk drives or to change memory requirements. The VM is an Ubuntu 12.04 long-term support installation containing OS-related applications and files.



Single-channel PicoFlexor with aft-end processor module for application development through standard 1-Gig Ethernet and JTAG I/O interfaces.

# PICOFLEXOR™ SOFTWARE DEVELOPER'S KIT

## HIGHLIGHTS

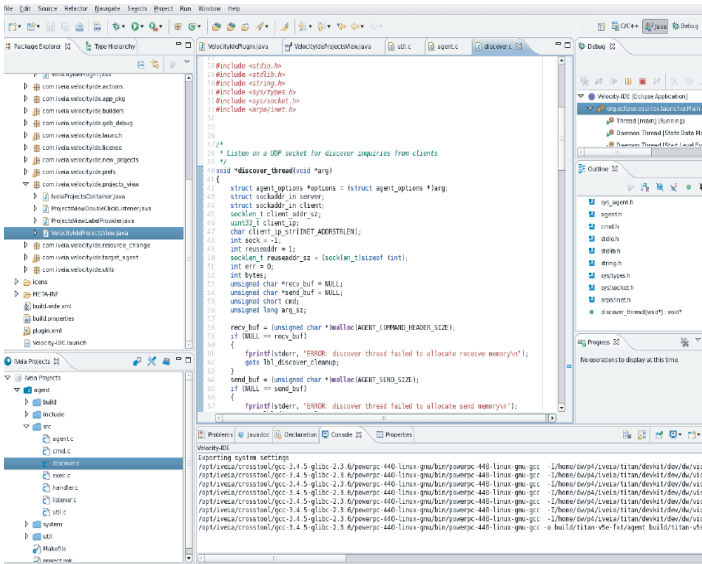
- FPGA application development, debugging, and deployment functionality in a single graphical environment
- Common application framework allows portability between PicoFlexor models
- Technology and version agnostic
- VMware Image of software tools for development of code for PicoFlexors
- Linux / bootloader source
- Velocity plug-in for Eclipse integrated development environment (IDE)
- Application Program Interface (API) for Software/Firmware applications development
- Documentation
- Sample code for beginners
- License for DRS Velocity-PDE

## HOST MACHINE REQUIREMENTS

- VMware Player (>4.0.0)
- Terminal program (Tera Term preferred)
- WinXP Pro (preferred)/Win 7 Pro (tested)
- >50 GByte Disk Space
- 1 GB Memory
- DVD Drive
- USB2.0 Port
- One serial port (but preferably two) or a USB-Serial adaptor

## ECLIPSE SYSTEM REQUIREMENTS

- 32-bit x86 Ubuntu 12.04 LTS
- iVeia Development Kit version 3.0 or greater
- Xilinx ISE Design Suite 14.1
- Eclipse version 3.6 (Helios) SR2



The PicoFlexor Software Developers' Kit comes complete with source code and tools required to perform FPGA application development.



The Software Developers' Kit is compatible with both the single and dual channel PicoFlexors

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