

Improving Allied Interoperability



GA-205 TIME DIVISION MULTIPLEXER

The DRS 12-channel GA-205 Time Division Multiplexer (TDM) provides full-duplex and half-duplex transmission and reception of data at selectable user port rates of 75×2^n up to 9,600 bps.

The aggregate data rates are selectable from 150 to 38,400 bps with an aggregate format that exceeds 93% efficiency. Various transmit aggregate clocking options are provided for operation with data modems or other transmission media.

The system accommodates user data that do not share common timing sources and provides for isochronous, bit stuff, synchronous and asynchronous operation. All digital data interfaces are operator configurable for RS-232S, RS-423A, RS-422A or mixed RS-422A/RS-423A.

Control of the GA-205 is through a RS-232 port opening operating at 9,600 bps asynchronous. Windows®-based remote control software is supplied.



12-Channel GA-205 Time Division Multiplexer

HIGHLIGHTS

- All firmware in flash memory
- RS-232C remote control port, RS-232C, RS-423A, RS-422/RS-423 user port interfaces
- User data rates up to 9,600 bps
- Aggregate data rates up to 38,400 bps
- Built-In Test (BIT)
- U.S. Navy HSFB format
- Upgrades in field with PC
- Loopback test modes
- Data sense programmable for positive or negative

PORT FEATURES

Number of user ports	Up to 12 full-duplex circuits utilizing time division multiplexing
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ASYNCHRONOUS

Data rates	50, 75, 100, 150, 300, 600, 1,200, 2,400, 4,800, 7,200 or 9,600 bps
Data bits	Programmable for 5, 6, 7 or 8
Stop bits	Programmable for 1.0, 1.42, 1.5 or 2.0

SYNCHRONOUS

Data rates	50, 75, 100, 150, 300, 600, 1,200, 4,800, 7,200 or 9,600 bps
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ISOCHRONOUS

Data rates	<75, <150, <300, <600, <1,200
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BIT-STUFFED

Data rates	50, 75, 100, 150, 300, 600, 1,200, 2,400, 4,800, 7,200 or 9,600 bps
Clock sense	Programmable for normal or inverted

AGGREGATE FEATURES

Data rates	150, 300, 600, 1,200, 2,400, 4,800, 7,200 or 9,600, 19,200, 38,400 bps
Transmit clock source	Programmable for internal timebase, transmit clock or receive clock

Internal time base	Programmable for internal oscillator (10 ppm) or station clock (1 or 5 MHz)
Synchronization character	Programmable for frame 1-9C16 or frame 2-9D16

BUILT-IN-TEST

Automatic power-on confidence test when power applied or upon operator-initiation

LOOPBACK

Aggregate	Aggregate transmit looped internally back toward aggregate receive
Port	Port transmit looped externally back toward port receive

SWAP

Height	One unit 1.73 inches (4.4 cm)
Width	19-inch rack slide mountable enclosure
Depth	17 inches (43.2 cm)
Power requirements	85/264 VAC, 47 to 440 Hz, 30 W maximum

RELIABILITY

MTBF @ 25% Naval sheltered 12,000 hrs, ground fixed 20,000 hrs per MIL-HDBK-217F, Notice 1
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ELECTROMAGNETIC COMPATIBILITY

Per MIL-STD-461, Part 4, Class A3 and Part 5, Class A4
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ENVIRONMENTAL

Temperature	
Storage	-4°F to +140°F (-20°C to +60°C)
Operating	+32°F to +122°F (0°C to +50°C)
Humidity	90% at +122°F (+50°C) non-condensing
Vibration	MIL-STD-165-1, Type 1
Shock	40g 6-9 ms half sine per MIL-STD-810E method 516.4 procedure 1



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